



**GP**  
**ELECTRONICS**

**GP3134KC-L**  
**20V N-Channel MOSFET**

### Product Summary

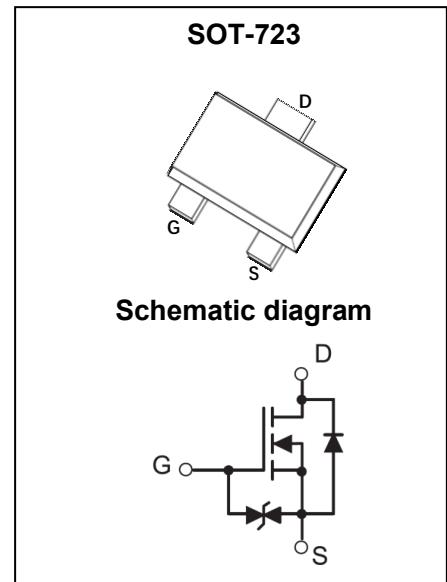
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
20V	150mΩ@4.5V	0.75A
	200mΩ@2.5V	
	270mΩ@1.8V	

### Feature

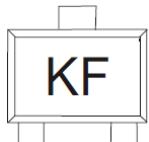
- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge
- ESD Protected

### Application

- Load Switching
- Low Current Inverters
- Low Current DC/DC Converters



### MARKING:



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	$V_{DS}$	20	V
Gate - Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current <sup>1,5</sup>	$I_D$	0.75	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	2.3	A
Power Dissipation <sup>4,5</sup>	$P_D$	0.2	W
Thermal Resistance from Junction to Ambient <sup>5</sup>	$R_{\theta JA}$	625	°C/W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55~+150	°C

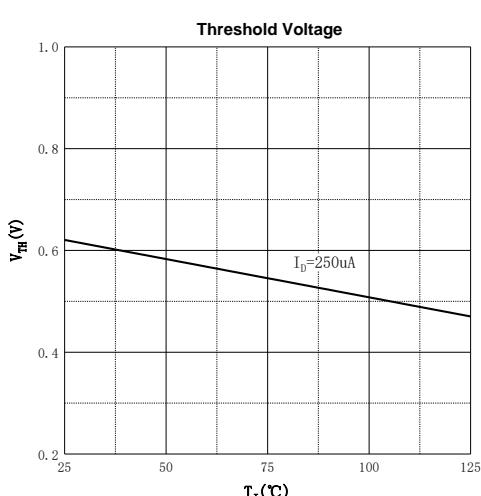
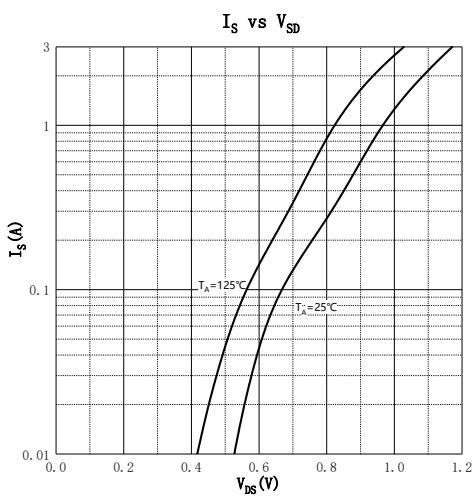
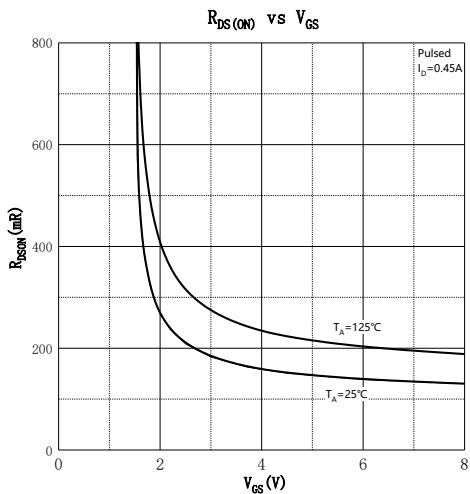
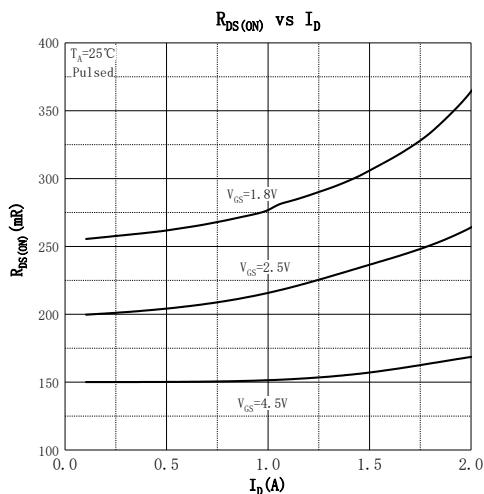
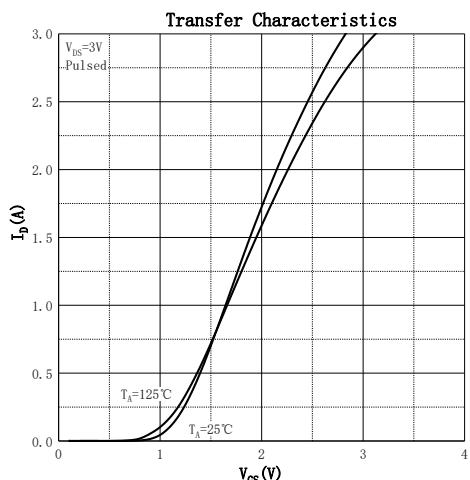
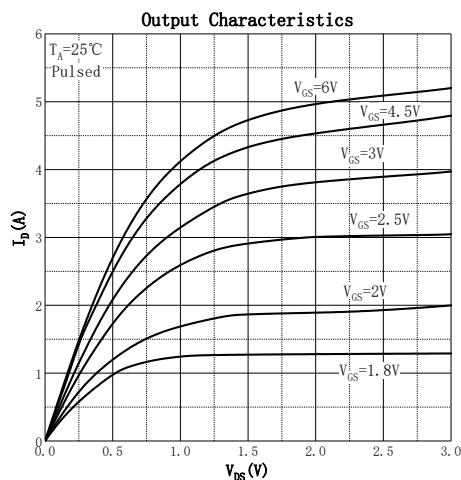
**MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$  unless otherwise noted)**

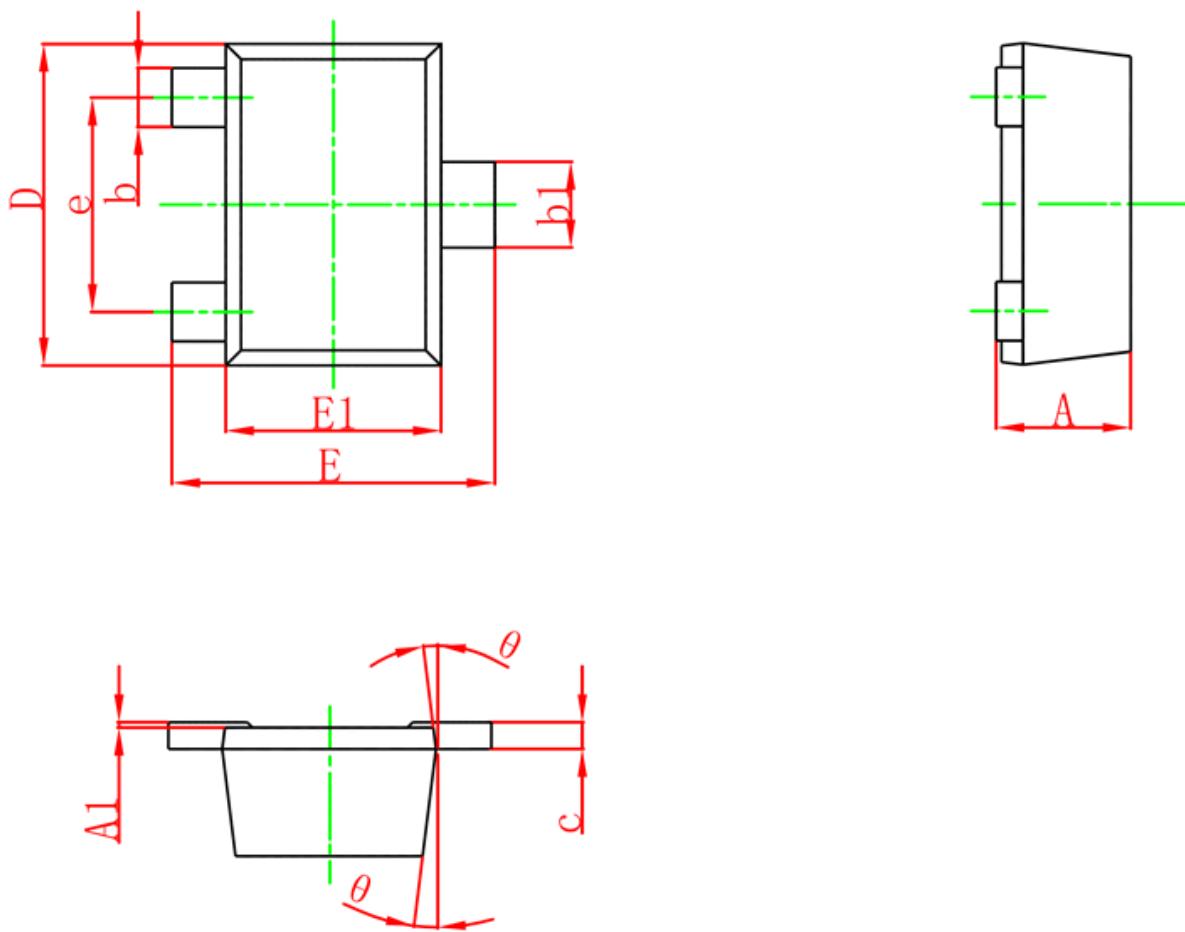
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 10$	$\mu\text{A}$
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.4	0.62	0.75	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 0.65\text{A}$		150	380	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 0.55\text{A}$		200	450	
		$V_{\text{GS}} = 1.8\text{V}, I_D = 0.45\text{A}$		270	800	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = 5\text{V}, I_D = 0.4\text{A}$	1			S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		60		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			13		
Reverse Transfer Capacitance	$C_{\text{rss}}$			11		
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, I_D = 0.4\text{A}$		2.13		$\text{nC}$
Gate-source Charge	$Q_{\text{gs}}$			1.12		
Gate-drain Charge	$Q_{\text{gd}}$			0.56		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, R_L = 25\Omega, R_G = 3\Omega$		6		$\text{ns}$
Turn-on Rise Time	$t_r$			5.5		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			27		
Turn-off Fall Time	$t_f$			12		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = 0.15\text{A}$			1.2	V

Notes :

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width  $\leq 10\mu\text{s}$ , duty cycle  $\leq 1\%$ .
- 3.Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- 4.The power dissipation  $P_D$  is limited by  $T_{J(\text{MAX})} = 150^\circ\text{C}$ . Pluse width is  $10\mu\text{s}$ ,duty cycle is 1%
- 5.Device mounted on  $1\text{in}^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

## Typical Characteristics



**SOT-723 Package Information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.340	0.500	0.013	0.020
A1	0.000	0.050	0.000	0.002
b	0.150	0.270	0.006	0.011
b1	0.200	0.370	0.008	0.015
c	0.060	0.160	0.002	0.006
D	1.100	1.300	0.043	0.051
E	1.100	1.300	0.043	0.051
E1	0.700	0.900	0.028	0.035
e	0.8TYP		0.031TYP	
θ	8°REF		8°REF	